

News Letter



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

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Student Interaction with Kazakhstani Educators

On 12 Feb 2026 interactive session led by Kazakhstani educators was organized by Department of electronics and Communication Engineering. It served as a vital platform for cross-cultural academic exchange, aiming to provide students with diverse educational perspectives and innovative learning approaches. By focusing on knowledge sharing and intercultural collaboration, the session bridged the gap between different academic backgrounds. Participants engaged in structured segments, including a formal welcome address and deep-dive discussions into modern education systems, student engagement techniques, and global learning opportunities.

A significant portion of the event was dedicated to interactive activities, such as Q&A rounds and problem-solving tasks, which spurred high levels of student enthusiasm. The cultural exchange segment was particularly impactful, allowing students and educators to share insights into their respective traditions and educational practices. This dialogue not only enhanced the participants' communication and critical thinking skills but also fostered a genuine mutual understanding and strengthened academic connections.

Despite minor challenges regarding language differences and time constraints, the session was a resounding success. The outcomes pointed toward a heightened awareness of international education practices and a strong interest in future collaborative efforts. To build on this momentum, recommendations include holding more frequent sessions, implementing structured breakout groups for more focused discussions, and providing pre-session materials to ensure participants are even better prepared for the exchange.



TECH LOCKDOWN

From Tambola to Triumph

TECH LOCKDOWN – From Tambola to Triumph, organized by the Anvitha Club at GL Bajaj Institute of Technology and Management on 25 Feb 2026, was an engaging three-round technical event aimed at promoting learning, teamwork, and strategic thinking among students. The event provided an interactive platform for participants to test their technical knowledge in a fun and competitive environment.

The event began with Round 1 – Tech Tambola, a creative variation of the traditional Tambola game where technical clues were given instead of numbers. Participants identified the correct technical terms and marked them on their sheets. This round tested students' quick thinking and basic technical knowledge while creating an energetic atmosphere.

The second round, Tech Grid Takeover, focused on teamwork. Students formed row-based teams and competed on a 5 × 5 technical grid containing different technical categories. Teams selected questions from the grid and earned points for correct answers. After a competitive session, the top four teams advanced to the final round.

The final round, Tech Auction War, added a strategic element to the competition. The four finalist teams were given points and had to bid to answer technical questions. Correct answers earned points, while incorrect answers resulted in deductions, making strategy and confidence key to winning.

The event saw enthusiastic participation from students across departments. It successfully encouraged technical learning, teamwork, and problem-solving in an enjoyable format. The winners were recognized for their performance, and all participants were appreciated for their active involvement in the event.



Industrial Visit to VVDN Technologies

The Department of Electronics and Communication organized an industrial visit under the Anvitha Club to VVDN Technologies Private Limited on 21st March 2026 at their Global Innovation Park (GIP), Manesar, Gurugram, which serves as their India headquarters. The visit was aimed at providing engineering students with practical exposure to bridge the gap between academic knowledge and industrial practices, particularly in the field of electronics manufacturing services (EMS) and product engineering. VVDN Technologies is a leading global provider of software, product engineering, and manufacturing solutions, specializing in advanced domains such as 5G and networking, AI/ML-based vision systems, IoT and cloud computing, and automotive electronics. During the visit, students were introduced to the company's design-led manufacturing approach, which integrates product design and manufacturing under one roof.

The students explored several key departments during the visit. In the Research and Development (R&D) section, they observed how hardware design, PCB layout, and embedded software development are carried out simultaneously to create efficient and innovative products. The SMT (Surface Mount Technology) lines were one of the highlights, where students witnessed processes such as solder paste printing, automated high-speed component placement, and reflow soldering. The use of Automated Optical Inspection (AOI) systems demonstrated how quality assurance is maintained for complex PCB assemblies. The visit also included the mechanical and enclosure design labs, where students learned about product durability testing under various environmental conditions such as thermal stress, vibration, and ingress protection. Additionally, the testing and certification labs showcased advanced EMI/EMC testing facilities that ensure products meet international regulatory standards before being deployed globally.

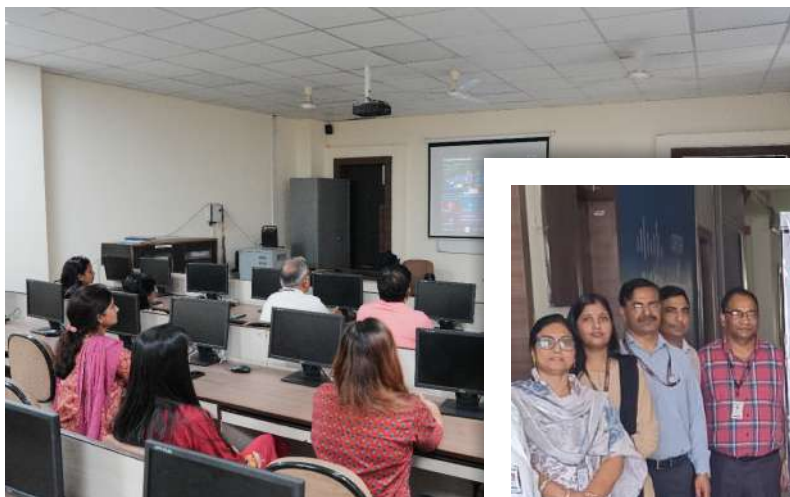
Through this visit, students gained valuable insights into the complete product lifecycle, from conceptual design to final manufacturing and packaging. They were also introduced to industry practices such as clean-room protocols, ESD safety measures, and adherence to ISO quality standards. Interaction with industry professionals provided guidance on essential skills required for careers in hardware engineering, embedded systems, and quality assurance. Overall, the industrial visit to VVDN Technologies was highly informative and enriching, offering a real-world perspective on modern engineering practices and highlighting the significance of interdisciplinary knowledge in today's rapidly evolving technological landscape. The experience also reflected the spirit of the "Make in India" initiative by showcasing advanced manufacturing capabilities within the country.



FDP on “Assessment and Evaluation” (ICT-o8)

The Department of Electronics and Communication Engineering, G. L. Bajaj Institute of Technology and Management, Greater Noida, successfully organized the Faculty Development Programme (FDP) on “Assessment and Evaluation” under TAC No. ICT-08 from 18th to 22nd May 2026 in hybrid mode. The programme was conducted by the Department of Education and Educational Management, National Institute of Technical Teachers Training & Research (NITTTR), with GLBITM serving as the nodal centre. The FDP was coordinated by Dr. Dinesh Kumar Singh (Nodal Centre Coordinator) and Dr. Smriti Sachan (Co-coordinator). A total of 24 faculty members from GLBITM actively participated in the programme. The FDP focused on strengthening faculty competencies in assessment and evaluation, outcome-based education, innovative pedagogical approaches, learner analytics, AI-enabled assessment tools, research evaluation, and question paper setting. Expert sessions were delivered by eminent academicians from reputed institutions, including Dr. Manoj Kumar, Dr. Rama Chhabra, Dr. Jatinder Grover, Dr. Sasirekha Selvakumar, Dr. Maitreyee Dutta, Dr. Niraj Bala, and Dr. Chandrasekhar Vazalwar. Participants gained valuable insights into effective student assessment practices, educational taxonomies, learning outcomes, practical evaluation methods, and the implementation of NEP-2020 based educational frameworks. The programme also highlighted the use of social media and Artificial Intelligence tools in teaching-learning and assessment processes. Hands-on activities, including test item design and participant presentations, enhanced collaborative learning and practical understanding.

Overall, the FDP was highly informative and beneficial, enriching participants' understanding of modern assessment methodologies and promoting the adoption of innovative, technology enabled teaching learning practices in higher education. The programme concluded successfully with positive feedback and appreciation from all participants.



Farewell - 2025-26

On 25th May 2026, the ANVITHA Club successfully organized the farewell ceremony for the outgoing students of the Department of Electronics and Communication Engineering (ECE). The event celebrated the seniors' journey through cultural performances, interactive activities, and heartfelt moments. The program began with a welcome address by the hosts, followed by inspiring speeches from the Head of Department, who congratulated the graduating students and wished them success in their future endeavors. The celebration featured vibrant dance performances, solo singing, group performances by juniors and seniors, and engaging fun activities that strengthened

the bond between students. A special recognition ceremony was conducted to honor the outgoing students with badges, titles, and awards. One of the most memorable highlights was the "Memory Down the Lane" presentation, showcasing photographs and videos that beautifully captured the seniors' college journey and evoked nostalgic emotions. The event concluded with a vote of thanks, followed by group photographs and informal interactions. Overall, the farewell was a memorable and successful celebration, leaving the seniors with cherished memories as they embarked on a new chapter of their lives.



Faculty Achievement

Dr. Satyendra Sharma
HoD
Department of ECE, GLBITM



The Department of Electronics and Communication Engineering proudly congratulates

Dr. Satyendra Sharma on the grant of an Indian Patent for the innovative invention titled "Water Quality and Service Integrity Analyzer." The patent (Patent No. 577842) was granted by the Patent Office, Government of India on 09 January 2026, based on the application filed on 14 June 2023.

This significant achievement reflects Dr. Sharma's commitment to research, innovation, and the development of technology-driven solutions for monitoring water quality and ensuring service integrity. The patented invention contributes to the advancement of sustainable and reliable water management systems and highlights the department's growing excellence in research and intellectual property creation.

The department extends its heartfelt congratulations to Dr. Satyendra Sharma and wishes him continued success in his future research endeavors.

We are proud to celebrate the remarkable achievement of Dr. Smriti Sachan, Associate Professor, Department of Electronics & Communication Engineering, G. L. Bajaj Institute of Technology and Management, who represented the institute at the prestigious Marie Curie Fellowship Program organized by the Ministry of Malta, Europe, on 16–17 June 2026.

Bringing together leading academicians, researchers, and innovators from across the globe, the fellowship focused on advancing research, fostering international collaboration, and promoting academic excellence. The program also explored global postdoctoral research opportunities and interdisciplinary partnerships that drive impactful innovation.

Dr. Sachan's participation marks another significant milestone in GL Bajaj's commitment to global academic engagement. The knowledge, insights, and international connections gained through this prestigious fellowship will strengthen research collaborations, enhance innovation, and contribute to the institute's vision of academic excellence. Congratulations, Dr. Smriti Sachan!

Dr. Smriti Sachan
Associate Professor
Department of ECE, GLBITM



Student Achievement



The Department of Electronics and Communication Engineering proudly congratulates Ms. Mahak Sharma on her outstanding placement at Autodesk with an impressive package of ₹16 LPA.

Her achievement is a testament to her dedication, technical excellence, perseverance, and unwavering commitment to excellence. This remarkable accomplishment not only reflects her hard work but also highlights the quality of education and mentorship provided by the department.

Mahak's success serves as an inspiration to her fellow students, motivating them to pursue their goals with confidence and determination. The Department extends its heartfelt congratulations to Mahak Sharma and wishes her continued success and many more achievements in her professional journey.

Editorial Board

Convenor : **Dr. Satyendra Sharma (HOD)**

Editors : **Dr. Dinesh Singh - Professor**
: **Dr. Shilpa Choudhary - Professor**

Vision

To become a Department of repute and prepare globally competent and socially responsible professionals with holistic knowledge and technical skills.

Mission

- To provide a supportive environment for effective knowledge transfer and nurturing ideas.
- To equip students with technical and leadership skills, and abilities to face the global challenges.
- To create an environment conducive for research and development with continuous involvement of industry.
- To inculcate professional and moral values in the students.

Program Educational Objectives (PEOs)

- Exhibit multi-disciplinary technical and leadership skills for better employability, higher education and entrepreneurship.
- Possess strong knowledge in the field of Electronics and Communication Engineering to develop solutions for real life problems.
- Undertake professional responsibilities through strong team-work, communication skills and life-long learning.
- Become socially responsible professionals with moral values and ethics.

